

**EAST Search History**

| Ref # | Hits | Search Query   | DBs   | Default Operator | Plurals | Time Stamp          |
|-------|------|--|---|------------------|---------|---------------------|
| L5    | 0    | 702/120.ccls.<br>and (test\$3 with<br>controller) and<br>(bypass with<br>register) and<br>(pin with (input<br>or output)) and<br>(bit with pattern<br>\$3) and (block<br>\$3 or filter\$3)<br>and update\$3<br>and evaluate\$3 | US_PGPUB;<br>USPAT;<br>USOCR; EPO;<br>JPO;<br>DERWENT;<br>IBM_TDB | OR               | ON      | 2009/06/07<br>11:33 |
| L6    | 0    | 702/108.ccls.<br>and (test\$3 with<br>controller) and<br>(bypass with<br>register) and<br>(pin with (input<br>or output)) and<br>(bit with pattern<br>\$3) and (block<br>\$3 or filter\$3)<br>and update\$3<br>and evaluate\$3 | US_PGPUB;<br>USPAT;<br>USOCR; EPO;<br>JPO;<br>DERWENT;<br>IBM_TDB | OR               | ON      | 2009/06/07<br>11:33 |
| L7    | 0    | 702/117.ccls.<br>and (test\$3 with<br>controller) and<br>(bypass with<br>register) and<br>(pin with (input<br>or output)) and<br>(bit with pattern<br>\$3) and (block<br>\$3 or filter\$3)<br>and update\$3<br>and evaluate\$3 | US_PGPUB;<br>USPAT;<br>USOCR; EPO;<br>JPO;<br>DERWENT;<br>IBM_TDB | OR               | ON      | 2009/06/07<br>11:33 |

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| L8  | 0 | 702/118.ccls.<br>and (test\$3 with<br>controller) and<br>(bypass with<br>register) and<br>(pin with (input<br>or output)) and<br>(bit with pattern<br>\$3) and (block<br>\$3 or filter\$3)<br>and update\$3<br>and evaluate\$3  | US_PGPUB;<br>USPAT;<br>USOCR; EPO;<br>JPO;<br>DERWENT;<br>IBM_TDB | OR  | ON | 2009/06/07<br>11:33 |
| L9  | 0 | 702/121.ccls.<br>and (test\$3 with<br>controller) and<br>(bypass with<br>register) and<br>(pin with (input<br>or output)) and<br>(bit with pattern<br>\$3) and (block<br>\$3 or filter\$3)<br>and update\$3<br>and evaluate\$3  | US_PGPUB;<br>USPAT;<br>USOCR; EPO;<br>JPO;<br>DERWENT;<br>IBM_TDB | OR  | ON | 2009/06/07<br>11:34 |
| L10 | 1 | "702"/\$.ccls.<br>and (test\$3 with<br>controller) and<br>(bypass with<br>register) and<br>(pin with (input<br>or output)) and<br>(bit with pattern<br>\$3) and (block<br>\$3 or filter\$3)<br>and update\$3<br>and evaluate\$3 | US_PGPUB;<br>USPAT;<br>USOCR; EPO;<br>JPO;<br>DERWENT;<br>IBM_TDB | OR<br><br>scan title, abstract, kwic, all | ON | 2009/06/07<br>11:34 |
| L11 | 0 | 700/1.ccls. and<br>(test\$3 with<br>controller) and<br>(bypass with<br>register) and<br>(pin with (input<br>or output)) and<br>(bit with pattern<br>\$3) and (block<br>\$3 or filter\$3)<br>and update\$3<br>and evaluate\$3    | US_PGPUB;<br>USPAT;<br>USOCR; EPO;<br>JPO;<br>DERWENT;<br>IBM_TDB | OR  | ON | 2009/06/07<br>11:35 |

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| L12 | 0 | 700/53.ccls. and (test\$3 with controller) and (bypass with register) and (pin with (input or output)) and (bit with pattern \$3) and (block \$3 or filter\$3) and update\$3 and evaluate\$3   | US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB | OR | ON | 2009/06/07 11:35 |
| L13 | 0 | "700"/\$.ccls. and (test\$3 with controller) and (bypass with register) and (pin with (input or output)) and (bit with pattern \$3) and (block \$3 or filter\$3) and update\$3 and evaluate\$3 | US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB | OR | ON | 2009/06/07 11:35 |
| L14 | 0 | 710/1.ccls. and (test\$3 with controller) and (bypass with register) and (pin with (input or output)) and (bit with pattern \$3) and (block \$3 or filter\$3) and update\$3 and evaluate\$3    | US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB | OR | ON | 2009/06/07 11:36 |
| L15 | 0 | 710/5.ccls. and (test\$3 with controller) and (bypass with register) and (pin with (input or output)) and (bit with pattern \$3) and (block \$3 or filter\$3) and update\$3 and evaluate\$3    | US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB | OR | ON | 2009/06/07 11:36 |

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| L16 | 0 | 710/14.ccls. and (test\$3 with controller) and (bypass with register) and (pin with (input or output)) and (bit with pattern \$3) and (block \$3 or filter\$3) and update\$3 and evaluate\$3 | US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB | OR | ON | 2009/06/07 11:36 |
| L17 | 0 | 710/18.ccls. and (test\$3 with controller) and (bypass with register) and (pin with (input or output)) and (bit with pattern \$3) and (block \$3 or filter\$3) and update\$3 and evaluate\$3 | US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB | OR | ON | 2009/06/07 11:36 |
| L18 | 0 | 710/20.ccls. and (test\$3 with controller) and (bypass with register) and (pin with (input or output)) and (bit with pattern \$3) and (block \$3 or filter\$3) and update\$3 and evaluate\$3 | US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB | OR | ON | 2009/06/07 11:36 |
| L19 | 0 | 710/36.ccls. and (test\$3 with controller) and (bypass with register) and (pin with (input or output)) and (bit with pattern \$3) and (block \$3 or filter\$3) and update\$3 and evaluate\$3 | US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB | OR | ON | 2009/06/07 11:37 |

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| L20 | 1 | 710/37.ccls. and (test\$3 with controller) and (bypass with register) and (pin with (input or output)) and (bit with pattern \$3) and (block \$3 or filter\$3) and update\$3 and evaluate\$3 | US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB | OR | ON | 2009/06/07 11:37 |
| L21 | 0 | 710/48.ccls. and (test\$3 with controller) and (bypass with register) and (pin with (input or output)) and (bit with pattern \$3) and (block \$3 or filter\$3) and update\$3 and evaluate\$3 | US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB | OR | ON | 2009/06/07 11:37 |
| L22 | 0 | 710/52.ccls. and (test\$3 with controller) and (bypass with register) and (pin with (input or output)) and (bit with pattern \$3) and (block \$3 or filter\$3) and update\$3 and evaluate\$3 | US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB | OR | ON | 2009/06/07 11:37 |
| L23 | 0 | 710/58.ccls. and (test\$3 with controller) and (bypass with register) and (pin with (input or output)) and (bit with pattern \$3) and (block \$3 or filter\$3) and update\$3 and evaluate\$3 | US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB | OR | ON | 2009/06/07 11:37 |

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| L24 | 0 | 710/65.ccls. and (test\$3 with controller) and (bypass with register) and (pin with (input or output)) and (bit with pattern \$3) and (block \$3 or filter\$3) and update\$3 and evaluate\$3   | US_PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB | OR | ON | 2009/06/07 11:38 |
| L25 | 2 | "710"/\$.ccls. and (test\$3 with controller) and (bypass with register) and (pin with (input or output)) and (bit with pattern \$3) and (block \$3 or filter\$3) and update\$3 and evaluate\$3 | US_PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB | OR | ON | 2009/06/07 11:38 |
| L26 | 3 | 714/30.ccls. and (test\$3 with controller) and (bypass with register) and (pin with (input or output)) and (bit with pattern \$3) and (block \$3 or filter\$3) and update\$3 and evaluate\$3   | US_PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB | OR | ON | 2009/06/07 11:38 |
| L27 | 4 | 714/724.ccls. and (test\$3 with controller) and (bypass with register) and (pin with (input or output)) and (bit with pattern \$3) and (block \$3 or filter\$3) and update\$3 and evaluate\$3  | US_PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB | OR | ON | 2009/06/07 11:41 |

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| L28 | 1 | 714/726.ccls.<br>and (test\$3 with<br>controller) and<br>(bypass with<br>register) and<br>(pin with (input<br>or output)) and<br>(bit with pattern<br>\$3) and (block<br>\$3 or filter\$3)<br>and update\$3<br>and evaluate\$3 | US-PGPUB;<br>USPAT;<br>USOCR; EPO;<br>JPO;<br>DERWENT;<br>IBM_TDB | OR | ON | 2009/06/07<br>11:43 |
| L29 | 1 | 714/727.ccls.<br>and (test\$3 with<br>controller) and<br>(bypass with<br>register) and<br>(pin with (input<br>or output)) and<br>(bit with pattern<br>\$3) and (block<br>\$3 or filter\$3)<br>and update\$3<br>and evaluate\$3 | US-PGPUB;<br>USPAT;<br>USOCR; EPO;<br>JPO;<br>DERWENT;<br>IBM_TDB | OR | ON | 2009/06/07<br>11:43 |
| L30 | 0 | 714/729.ccls.<br>and (test\$3 with<br>controller) and<br>(bypass with<br>register) and<br>(pin with (input<br>or output)) and<br>(bit with pattern<br>\$3) and (block<br>\$3 or filter\$3)<br>and update\$3<br>and evaluate\$3 | US-PGPUB;<br>USPAT;<br>USOCR; EPO;<br>JPO;<br>DERWENT;<br>IBM_TDB | OR | ON | 2009/06/07<br>11:44 |
| L31 | 0 | 714/735.ccls.<br>and (test\$3 with<br>controller) and<br>(bypass with<br>register) and<br>(pin with (input<br>or output)) and<br>(bit with pattern<br>\$3) and (block<br>\$3 or filter\$3)<br>and update\$3<br>and evaluate\$3 | US-PGPUB;<br>USPAT;<br>USOCR; EPO;<br>JPO;<br>DERWENT;<br>IBM_TDB | OR | ON | 2009/06/07<br>11:44 |

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| L32 | 16 | "714"/\$.ccls.<br>and (test\$3 with<br>controller) and<br>(bypass with<br>register) and<br>(pin with (input<br>or output)) and<br>(bit with pattern<br>\$3) and (block<br>\$3 or filter\$3)<br>and update\$3<br>and evaluate\$3 | US_PGPUB;<br>USPAT;<br>USOCR; EPO;<br>JPO;<br>DERWENT;<br>IBM_TDB | OR | ON                              | 2009/06/07<br>11:44 |
|     |    |   |   |    | scan title, abstract, kwic, all |                     |
| L33 | 43 | (test\$3 with<br>controller) and<br>(bypass with<br>register) and<br>(pin with (input<br>or output)) and<br>(bit with pattern<br>\$3) and (block<br>\$3 or filter\$3)<br>and update\$3<br>and evaluate\$3                       | US_PGPUB;<br>USPAT;<br>USOCR; EPO;<br>JPO;<br>DERWENT;<br>IBM_TDB | OR | ON                              | 2009/06/07<br>11:45 |
|     |    |   |   |    | scan title, abstract, kwic, all |                     |
| L34 | 20 | waayers.in. and<br>thomas   | US_PGPUB;<br>USPAT;<br>USOCR; EPO;<br>JPO;<br>DERWENT;<br>IBM_TDB | OR | ON                              | 2009/06/07<br>12:01 |
|     |    |   |   |    | scan title, abstract, kwic, all |                     |
| L35 | 1  | 34 and (test\$3<br>with controller)<br>and (bypass<br>with register)<br>and (pin with<br>(input or<br>output)) and (bit<br>with pattern\$3)<br>and (block\$3 or<br>filter\$3) and<br>update\$3 and<br>evaluate\$3               | US_PGPUB;<br>USPAT;<br>USOCR; EPO;<br>JPO;<br>DERWENT;<br>IBM_TDB | OR | ON                              | 2009/06/07<br>12:01 |
|     |    |   |   |    | scan title, abstract, kwic, all |                     |
| L36 | 0  | konninklijke adj<br>philips adj<br>electronics adj<br>nv.as.  | US_PGPUB;<br>USPAT;<br>USOCR; EPO;<br>JPO;<br>DERWENT;<br>IBM_TDB | OR | ON                              | 2009/06/07<br>12:06 |

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| L37 | 2    | konninklijke adj<br>philips adj<br>electronics.as.  | US_PGPUB;<br>USPAT;<br>USOCR; EPO;<br>JPO;<br>DERWENT;<br>IBM_TDB | OR<br><br>scan title, abstract, kwic, all | ON | 2009/06/07<br>12:07 |
| L38 | 2478 | nxp adj bv.as.  | US_PGPUB;<br>USPAT;<br>USOCR; EPO;<br>JPO;<br>DERWENT;<br>IBM_TDB | OR  | ON | 2009/06/07<br>12:07 |
| L39 | 0    | 38 and (test\$3<br>with controller)<br>and (bypass<br>with register)<br>and (pin with<br>(input or<br>output)) and (bit<br>with pattern\$3)<br>and (block\$3 or<br>filter\$3) and<br>update\$3 and<br>evaluate\$3   | US_PGPUB;<br>USPAT;<br>USOCR; EPO;<br>JPO;<br>DERWENT;<br>IBM_TDB | OR  | ON | 2009/06/07<br>12:07 |
| L40 | 0    | (702/120.ccls. or<br>710/48.ccls.)<br>and ((test\$3<br>with controller)<br>and (bypass<br>with register)<br>and (pin with<br>(input or<br>output)) and (bit<br>with pattern\$3)<br>and (block\$3 or<br>filter\$3) and<br>update\$3 and<br>evaluate\$3).<br>CLM. | US_PGPUB;<br>USPAT;<br>USOCR; EPO;<br>JPO;<br>DERWENT;<br>IBM_TDB | OR<br><br>scan                            | ON | 2009/06/07<br>12:08 |

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